

Fig. 1

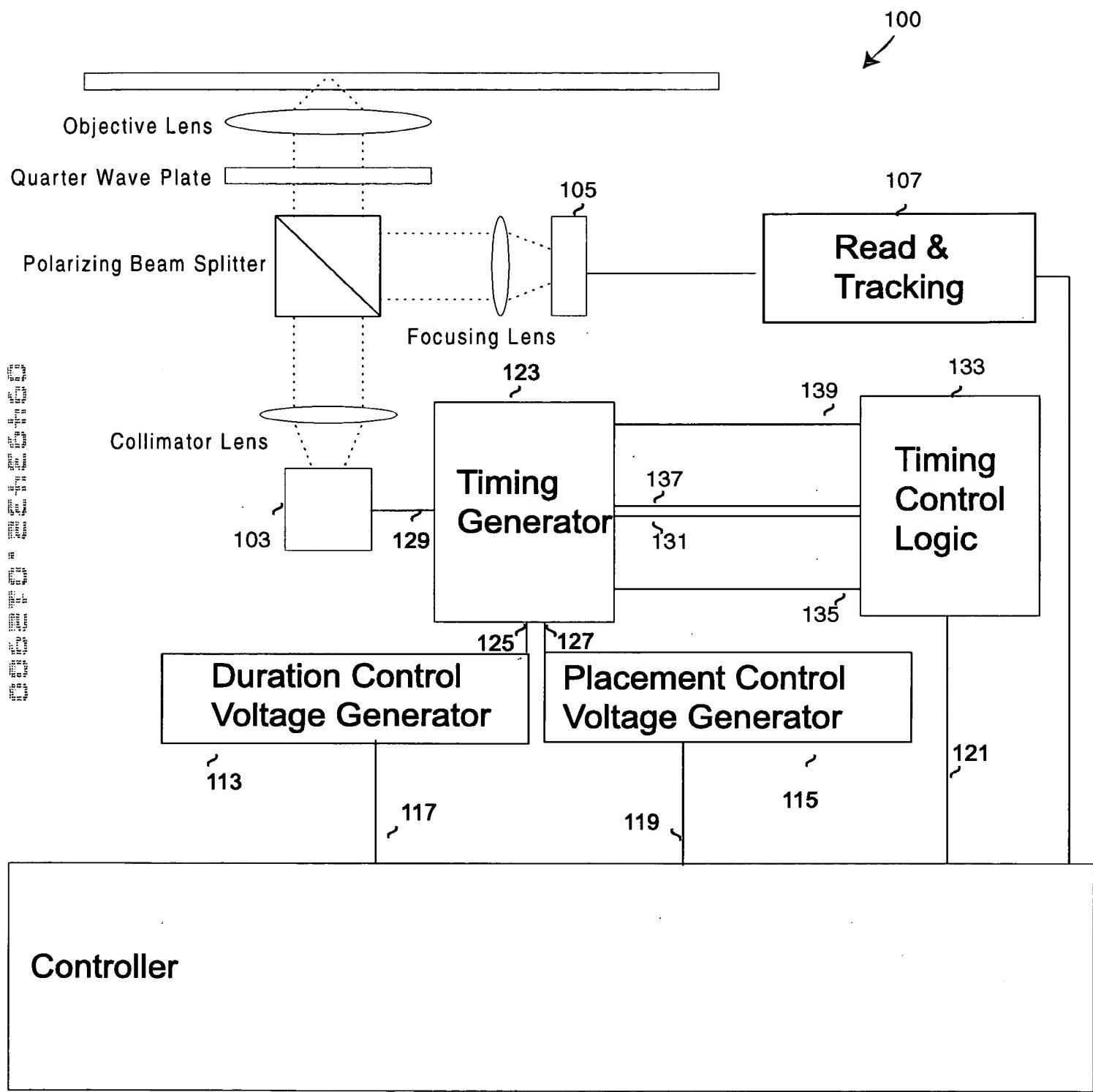


FIG. 2A

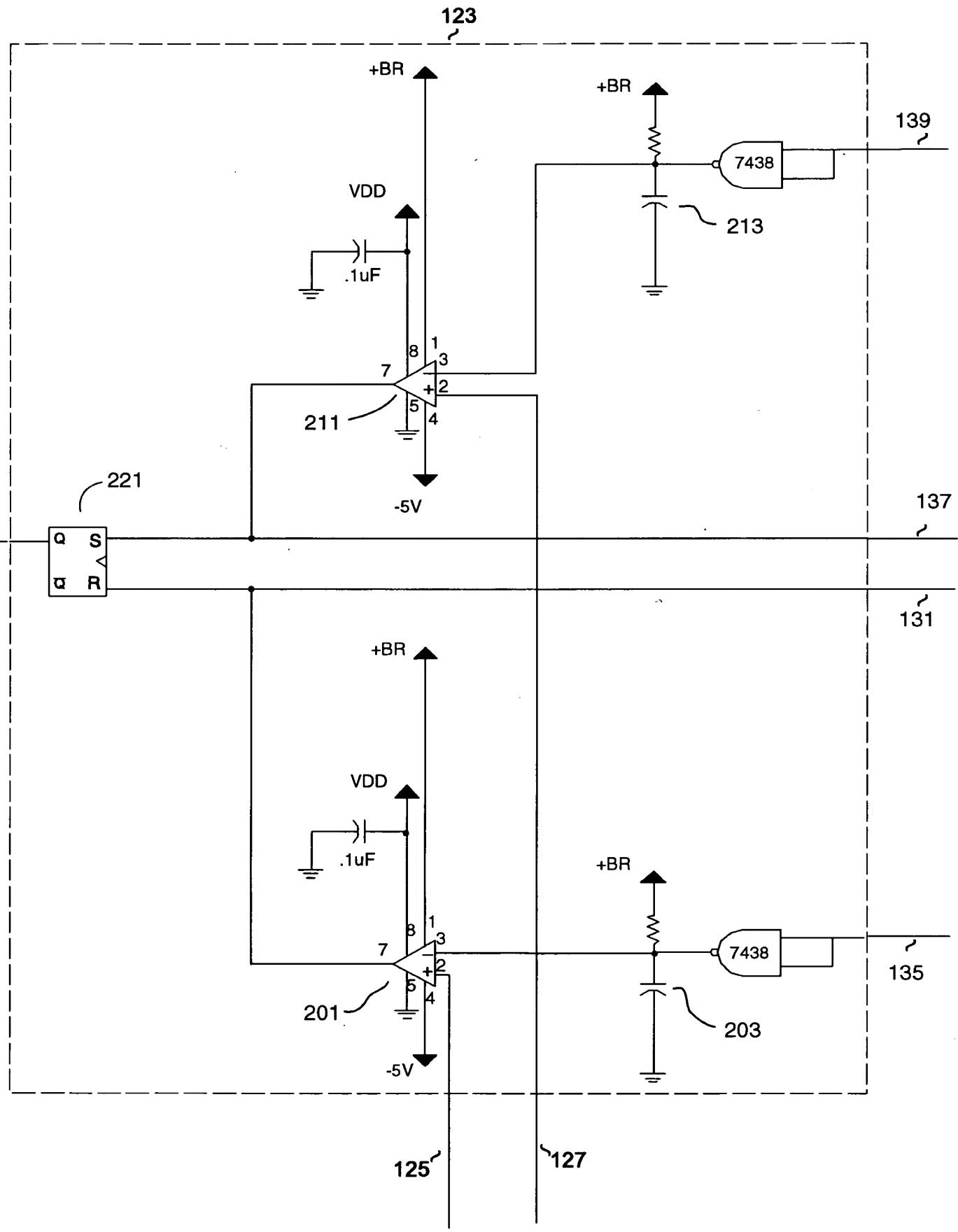


FIG. 2B

133

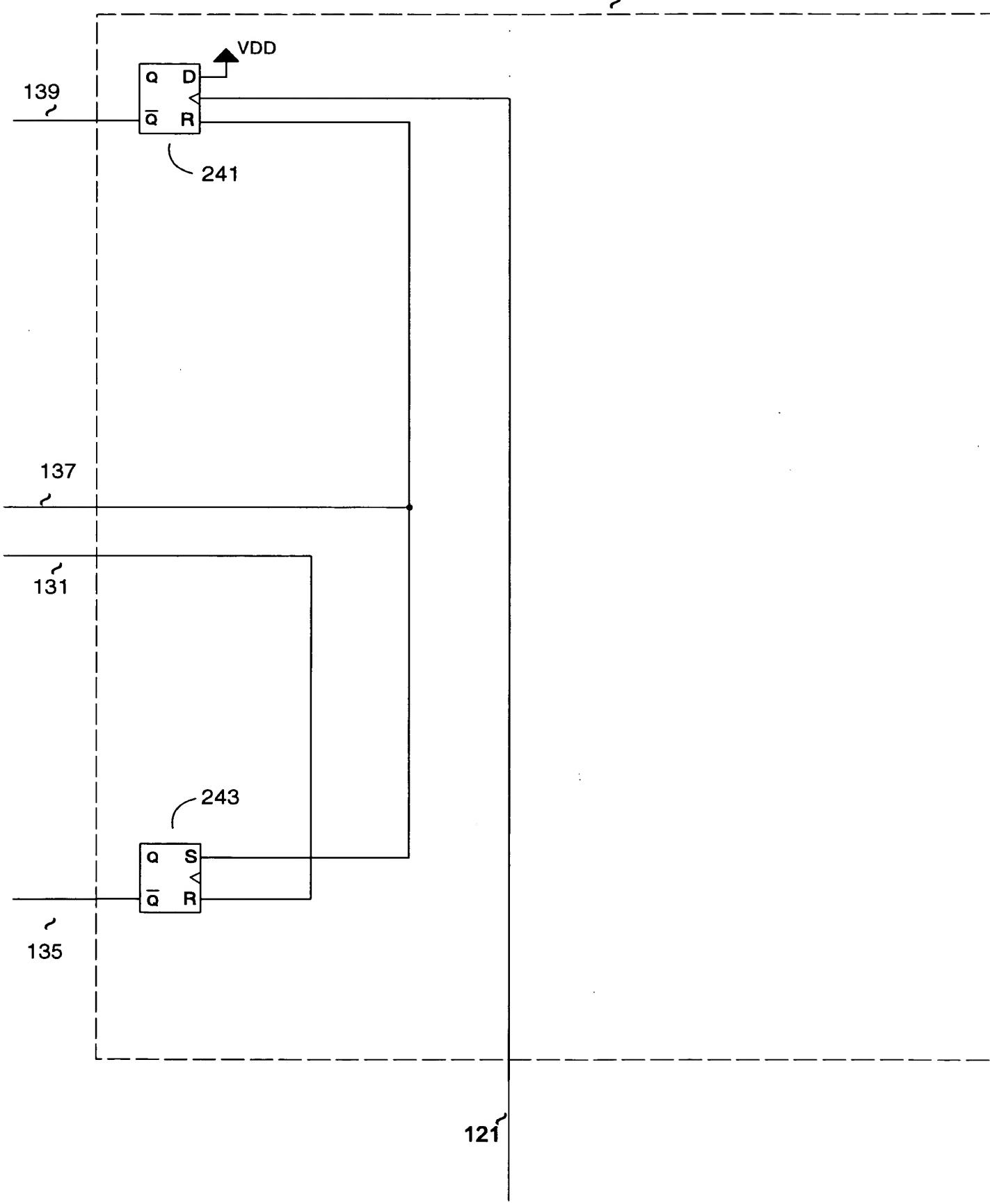


FIG. 3

Write Clock
Analog Placement Control Voltage
Analog Duration Control Voltage
Duration Cap Voltage

